

<b>Notice of References Cited</b>	Application/Control No. 10/019,059	Applicant(s)/Patent Under Reexamination DALTON, DAMIAN	
	Examiner Akash Saxena	Art Unit 2128	Page 1 of 2

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
✕	A	US-6,226,710	05-2001	Melchior, Timothy A.	711/108
✕	B	US-5,423,015	06-1995	Chung, David S. F.	711/108
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
✕	U	An associative memory approach to parallel logic event-driven simulation; Dalton, D.;CompEuro '92 . 'Computer Systems and Software Engineering',Proceedings.;4-8 May 1992;Page(s): 341-346□□ (NPL) .
	V	Model of auto associative memory that stores and retrieves data regardless of their orthogonality, randomness or size; Bairaktaris, D.;System Sciences, Proceedings of the Twenty-Third Annual Hawaii Intl Conf on Volume: i 2-5 Jan 1990 Page(s): 142-150
	W	A general-purpose CMOS associative processor IC and system; Stormon, C.D. et al;Micro, IEEE Volume 12, Issue 6, Dec. 1992 Page(s):68 - 78
	X	Associative Memories and Processors: An Overview and Selected Bibliography; Parhami B; IEEE 1973

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

<b>Notice of References Cited</b>	Application/Control No. 10/019,059	Applicant(s)/Patent Under Reexamination DALTON, DAMIAN	
	Examiner Akash Saxena	Art Unit 2128	Page 2 of 2

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Content-addressable and associative memory: alternatives to the ubiquitous RAM□□Chisvin, L.; Duckworth, R.J.; Computer Volume 22, Issue 7, July 1989 Page(s):51 - 64
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.